

**BVRIT HYDERABAD College of Engineering for Women**  
**AUTONOMOUS**  
**(Approved by AICTE, Affiliated to JNTUH)**  
**(Accredited by NBA – EEE, ECE, CSE & IT and NAAC with ‘A’ Grade)**

**Event Name:** Lecture on Introduction to VLSI Signal Processing

**Date (s) of Conduction:** 27-03-2024

**Name of the Resource Person with Details (if any) :** Associate Professor Dr. Asral Bahari Jambek, SMIEEE, Head, Centre of Excellence Micro System Technology (MiCTEC) Universiti Malaysia Perlis, Pauh Putra Campus.

**No. of Participants:** 96

**Organized by:** Department of Electronics and Communication Engineering,  
BVRIT HYDERABAD College of Engineering for Women.

**About the Event:**

The lecture on "Introduction to VLSI Signal Processing" delivered by Associate Professor Dr. Asral Bahari Jambek was conducted on 2<sup>nd</sup> March 2024. The session aimed to provide participants with a foundational understanding of VLSI (Very Large-Scale Integration) signal processing, exploring its fundamental concepts, applications, and relevance in modern technology.

**Content Overview:**

Basic Concepts of VLSI: Professor Asral began by elucidating the fundamental principles of VLSI, emphasizing its role in integrating thousands to millions of electronic components on a single chip.

- **Signal Processing Fundamentals:** The lecture delved into the core concepts of signal processing, including signal representation, analysis, and manipulation.
- **Applications of VLSI Signal Processing:** Various applications of VLSI signal processing were discussed, spanning diverse fields such as telecommunications, image processing, and biomedical engineering.
- **Recent Advancements and Future Trends:** Professor Asral provided insights into recent advancements in VLSI signal processing and speculated on future trends, highlighting the potential for innovation and research in the field.

**Engagement and Interaction:**

The lecture fostered active engagement and interaction among participants. Attendees posed questions, seeking clarification on complex concepts and applications, which

Professor Asral addressed this with clarity and expertise. The interactive nature of the session facilitated a dynamic learning environment, encouraging participants to actively participate and contribute to discussions.

**Key Takeaways:**

- A clear understanding of VLSI signal processing principles and applications.
- Insight into the significance of VLSI technology in modern electronic systems.
- Awareness of recent advancements and future directions in the field.
- Enhanced critical thinking and problem-solving skills through interactive discussions and Q&A sessions.

**Conclusion:**

The lecture on "Introduction to VLSI Signal Processing" delivered by Associate Professor Dr. Asral Bahari Jambek was highly informative and engaging, providing participants with a comprehensive overview of VLSI signal processing concepts and applications. The session not only enriched participants' knowledge but also inspired further exploration and research in this dynamic field.

**Recommendations:**

- Consider organizing follow-up sessions or workshops to delve deeper into specific aspects of VLSI signal processing.
- Explore opportunities for collaborative research projects or academic initiatives in collaboration with Professor Asral and other experts in the field.
- Gather feedback from participants to identify areas for improvement and tailor future lectures or sessions to meet the diverse needs of the academic community.

This report serves to document the proceedings and outcomes of the lecture, underscoring its significance in advancing knowledge and fostering academic collaboration in the field of VLSI signal processing.

**Photos:**

Department of ECE  
BVRIT HYDERABAD College of Engineering for Women  
Presents  
A SESSION ON  
**INTRODUCTION TO  
VLSI SIGNAL PROCESSING**  
by  
**DR. ASRAL BAHARI JAMBEK**  
Head, Centre of Excellence Micro System  
Technology (MICTEC)  
Universiti Malaysia Perlis, Pauh Putra Campus,  
MALAYSIA

**Details of the Session**  
DATE: 27/03/2024  
TIME: 2:00 PM TO 4:00 PM IST  
GOOGLE MEET: [HTTPS://MEET.GOOGLE.COM/AGE-YVQ-NFVZHS-JZ4](https://meet.google.com/AGE-YVQ-NFVZHS-JZ4)



**Faculty Co-ordinator**

**Head of the Department**