

### ***Personal Information***

Name	Dr. Anwar Bhasha Pattan
Years of Experience	Teaching: 16+Years
Email Id	<a href="mailto:anwarbhasha.p@bvrithyderabad.edu.in">anwarbhasha.p@bvrithyderabad.edu.in</a> <a href="mailto:ece.hod@bvrithyderabad.edu.in">ece.hod@bvrithyderabad.edu.in</a>
Areas of Specialization	VLSI and Embedded Systems



### ***Educational Qualifications***

Doctoral Degree	Ph.D.	Electronics and Communication Engineering
PG Degree	M.Tech.	VLSI System Design
UG Degree	B.Tech.	Electronics and Communication Engineering

### ***Papers Published/Presented***

#### ***International Journals***

1. “Booting Testcase Development for XILINX FPGA Devices”, International Journal of Scientific Research in Engineering and Management, Vol. 4, Issue. 5, May 2020.
2. “Low Power and High Performance Structures for Fast Fourier Transform Processor”, International Journal of Computer Applications, Vol 132-No.10, PP. 27-29, December, 2015. ISSN: 0975-8887.
3. “Fast Fourier Transform Architectures: A Survey and State of the Art”, International Journal on Electronics & Communication Technology, Vol 5, Issue 4, Version 1, pp.94-98, Oct-Dec, 2014. ISSN: 2230-7109(online), 2230-9543(print)
4. “Design of Flood Warning System based on F-bus Protocol”, CiiT International Journal of Networking and Communication Engineering, Vol 4, No 15, pp. 921-924, December 2012. ISSN: 0974-9713.
5. “The Terminal System Design Based on hybrid RFID-GPS in vehicular communications”, International Journal of Modern Engineering Research IJMER, Vol. 2, Issue 4, July-August, 2012, pp-2316-2319, ISSN: 2249-6645.
6. “Implementation of VoIP Using Session Initiation Protocol”, CiiT International Journal of Networking and Communication Engineering, Vol 3, No 7, PP. 481-486, May 2011. ISSN: 0974-9713.

### *International Conferences*

1. “ML based comparative analysis of Interconnect RC estimation in progressive Stacked circuits”, International Conference on Evolutionary Computing and Mobile Sustainable Networks (ICECMSN -2021) at RV Institute of Technology and Management, Bengaluru during 28-29, September, 2021. Published in LNDECT, Volume 116, Springer on 22-03-2022.
2. “FPGA implementation of a 64 point radix-2 single path delay feedback FFT architecture”, International Academic and Research Conference 2015 (IARC India 2015) in support of UNESCO’s 70<sup>th</sup> anniversary celebrations, 9-10, October, 2015, Vijayawada and published in the Engineering Sciences International Research Journal, IMRF Journals, Vol.1, Issue.2, August, 2015. ISSN: 2320 – 4338. Abstract published in proceedings with ISBN NO. 978-93-84124-31-1.
3. “Complexity Analysis of an 8 point FFT Processor for different Butterfly Structures”, International conference on Electrical, Electronics and Instrumentation Engineering (ICEEI-Aug’2015), 20-22, August, 2015, Colombo, Sri Lanka and published in the IJCEEE, Vol.3, Issue.2, pp.5-8, August, 2015. ISSN: 2345 – 9603.

### *National Conferences*

1. “FPGA implementation of an efficient radix-2<sup>3</sup> FFT algorithm”, National Conference on Emerging Trends in Information, Digital and Embedded Systems (NC’e-TIDES-15), 28<sup>th</sup> Feb, 2015, AITS, Rajampet. Published proceedings with ISBN No: 978-1-926488-02-8.
2. “Design of MAC unit using Reversible Logic”, National Conference on Electrical Sciences (NCES-12), 7<sup>th</sup> & 8<sup>th</sup> September, 2012, AITS, Rajampet, YSR Dt.
3. “A Novel Technique to improve register file immunity against soft errors”, National Conference on Emerging Trends in Information, Digital and Embedded Systems (NC’e-TIDES-12), 13<sup>th</sup> & 14<sup>th</sup> July, 2012, AITS, Rajampet, YSR Dt.
4. “Bidirectional Barrel shifter Design using reversible gates for low power applications”, National level Conference on emerging trends in Computers, Electronics and Power Engineering (NCECEP-2012), 1<sup>st</sup> & 2<sup>nd</sup> June, 2012, Audisankara College of Engineering and Technology, Gudur, Nellore Dt.
5. “Universal Remote Controller for Home Appliances”, National Conference on Assistive Technology, 29<sup>th</sup> - 31<sup>st</sup> July, 2011, Padmasri Dr. B.V. Raju Institute of Technology, Vishnupur, Narsapur, Medak Dt.
6. “Design and Implementation of FFT Processor for OFDMA System Using FPGA”, Fourth National Conference on Digital Convergence, FNCDC 2011, 16<sup>th</sup> April, 2011,

RMK Engineering College, Tamil Nadu. Published in proceedings with ISBN: 978-81-920889-3-8, pp.533-536.

7. “Black Box for Vehicle Diagnosis”, Fourth National Conference on Digital Convergence, FNCDC 2011, 16<sup>th</sup> April, 2011, RMK Engineering College, Tamil Nadu. Published in proceedings with ISBN: 978-81-920889-3-8, pp. 517-520.
8. “Group elevator scheduling with advance information for normal and emergency modes”, National Conference on Advancement and future trends in VLSI Design, NCVD 2011, 30<sup>th</sup> & 31<sup>st</sup> March, 2011, Kalasalingam University, Tamil Nadu.
9. “Design of On chip Bus with OCP Interface”, National Conference on Electronics and Communication Systems, 16<sup>th</sup> March, 2011, MITS, Madanapalle.
10. “Low Cost Extendable Framework for embedded smart car security system”, National Conference on Recent trends in ECE, Elixir 2k10, 8<sup>th</sup> & 9<sup>th</sup> December, SIETK, Puttur.
11. “Configurable Local link CRC Reference Design”, National Conference on Recent trends in ECE, Elixir 2k10, 8<sup>th</sup> & 9<sup>th</sup> December, 2010, SIETK, Puttur.
12. “An Efficient Multiplier Design using Reversible Logic Gates”, National Conference on Advances in Communication Technologies, NCACT-2010, 3<sup>rd</sup> & 4<sup>th</sup> December, 2010, GITAM University, Visakhapatnam.
13. “Design and simulation of Data to clock phase alignment”, National Conference on Future challenges and Building Intelligent Techniques in Electrical and Electronics Engineering, NCEEE-2010, 29<sup>th</sup> & 30<sup>th</sup> July, 2010, Sathyabama University, Chennai.
14. “Low Speed Buses in Embedded System Designs”, National Conference on Communication Technologies, NCComTech-2008, 28<sup>th</sup> & 29<sup>th</sup> February, 2008, GVPCOE, Visakhapatnam.

### ***FDPs/Workshops Attended /Conducted***

#### ***Conducted***

1. Organized a two week Faculty Development Program on Entrepreneurship sponsored by NSTEDB and co-organized by CED, Hyderabad during 25<sup>th</sup> – 30<sup>th</sup> March, 2019.
2. Entrepreneurship Awareness Camp during 7<sup>th</sup> to 9<sup>th</sup> February, 2019 sponsored by NSTEDB.
3. An expert talk on Entrepreneurship by Sri. Ramesh Vemuganti on 8<sup>th</sup> August, 2018.
4. Entrepreneurship Awareness Camp during 30<sup>th</sup> October to 1<sup>st</sup> November, 2017 in association with EDC, JNTUH, supported by NSTEDB.

5. One day workshop on Social Entrepreneurship by Dr. Hafeez Basha, CTO, BRCORP on 21<sup>st</sup> October, 2017.
6. Organized a one week workshop on Embedded System Design and Development for III B.Tech students during 6<sup>th</sup> – 11<sup>th</sup> March, 2017 in association with Armtronics Pvt Ltd, Hyderabad.

### *Attended*

1. NAAC Sponsored Two day National Seminar on “Role of IQAC in Evaluation and Analysis of Academic and Administration Audit for the Excellence in Higher Educational Institute in view of NAAC framework” organized by IQAC, Siddartha Institute of Science and Technology, Puttur on 17th & 18th June, 2022.
2. A national level online workshop on “Origin Software”, Organized by department of Basic Science and Humanities, BVRITH CEW, Hyderabad on 28<sup>th</sup> May, 2022.
3. An FDP on Orientation Training Programme for Mentors organized by NITTTR Kolkata from 09th to 13th February 2021
4. A Five day faculty development programme on “How to write Good research article and proposal”, Organized by BVRITH CEW, Hyderabad during 18-22 Jan, 2021
5. An FDP on “Fiddle with Virtual Components”, Organized by department of ECE, BVRITH CEW during 4-5 September, 2020
6. An FDP on “Wireless Communications and Mobile Technologies”, Organized by department of ECE, BVRITH CEW in association with QCARES, Qualcomm during 06-10 July, 2020
7. An FDP on “VLSI Design Flow”, Organized by Anurag University during 28-30 May, 2020.
8. Attended 13 sessions on IIC online sessions organized by MHRD Innovation cell from 28th April to 22nd May 2020.
9. An FDP on “Research Methodology in Research”, Organized by IEEE SB BVRIT HYDERABAD College of Engineering for Women in association with CIS/GRSS chapter IEEE Hyderabad section during 28 – 29 May, 2020
10. Workshop on “NAAC’s New Framework for Accreditation & Assessment of HEIs: Opportunities & Challenges”, during 19-21 September, 2019 at GNITS, Hyderabad.
11. Workshop on “Unconscious Bias in the Workplace” at VEDIC during 8<sup>th</sup> August, 2019.
12. Workshop on “Curriculum Design” at VEDIC during 18-19 July, 2019.

13. Faculty Development Program on Entrepreneurship sponsored by NSTEDB and jointly organized by CED, Hyderabad and BVRIT HYDERABAD CoE, Hyderabad during 18<sup>th</sup> – 30<sup>th</sup> March, 2019.
14. III workshop conducted by VEDIC during 18<sup>th</sup> – 20<sup>th</sup> March, 2019.
15. Workshop on “Curriculum Design” at VEDIC during 26-27 June, 2018.
16. MathWorks training on “DSP for FPGAs” during 19<sup>th</sup> to 21<sup>st</sup> June, 2017, Hyderabad.
17. Participated in the International Conference “VLSID-2017” with fellowship during 7<sup>th</sup> – 11<sup>th</sup> January, 2017.
18. workshop on “Scientific Educational Practices” during 7<sup>th</sup> to 9<sup>th</sup> December, 2016 at VEDIC, Aziz Nagar, Hyderabad.
19. A one week FDP on “Open Source Software through Spoken Tutorials, IIT Bombay” from 4<sup>th</sup> November to 10<sup>th</sup> November, 2016 at BVRITH, Hyderabad.
20. A one day workshop on “Advanced Programming Techniques with MATLAB” held on 16<sup>th</sup> September, 2016 at JNTUHCE, Hyderabad.
21. Participated in NIDays conducted by National Instruments India on 19<sup>th</sup> November, 2015.
22. A five day continuing education and training programme on “Low power System on Chip design using Xilinx FPGA Devices” during 3<sup>rd</sup> to 7<sup>th</sup> October, 2015 at NIT, Warangal.
23. A one day workshop on “Signal Processing, Communications and Computer Vision System Design using MATLAB and Simulink-2015” (SPCCVS) on 11<sup>th</sup> September, 2015 at JNTUH, Hyderabad.
24. Participated in the Engineering Education & Research Seminar conducted by National Instruments India on 30<sup>th</sup> April, 2015.
25. Participated in the “3<sup>rd</sup> International Conference on Micro Air Vehicles (ICRAMAV-2014)” during 5 – 7 November, 2014 at JNTUH, Hyderabad.
26. One day IEEE authorship workshop titled “How to publish a paper with IEEE” on 14<sup>th</sup> August, 2014 at University of Hyderabad, Hyderabad.
27. One day complementary seminar on “MATLAB and Simulink for Engineering Education” on 11<sup>th</sup> August, 2014 in Hyderabad.
28. A Two day workshop on “Analog and Digital IC Design using Cadence Tools” on 10<sup>th</sup> and 11<sup>th</sup> July, 2014 at Easwari Engineering College, Chennai.

29. One day awareness workshop on “Intellectual Property Rights” on 26<sup>th</sup> March, 2014 at JNTUHCE, Hyderabad.
30. Participated in the “1<sup>st</sup> National Conference on Robotics, Automation and Mechatronics (NCRAM-2014)” on 12<sup>th</sup> and 13<sup>th</sup> March, 2014 at JNTUH, Hyderabad.
31. Participated in the “2<sup>nd</sup> International Conference on Micro Air Vehicles (ICRAMAV-2013)” during 21 – 23 November, 2013 at JNTUH, Hyderabad.
32. One day symposium on “Micro Air Vehicles” on 27<sup>th</sup> September, 2013 at JNTUHCE, Hyderabad.
33. A Two Weeks AICTE sponsored faculty development programme on “Advanced EDA Tools for VLSI Design” during 16 – 28 September, 2013 at ASIT, Gudur, SPSR Nellore Dt.
34. A Two Weeks AICTE sponsored faculty development programme on “VLSI Design” from 20<sup>th</sup> May to 1<sup>st</sup> June, 2013 at VNRVJIET, Hyderabad.
35. Participated in the 25<sup>th</sup> International conference on “VLSI Design” from 7<sup>th</sup> to 11<sup>th</sup> January, 2012 at HICC, Hyderabad.
36. A two day national level workshop on “Research Methodology in Engineering” on 10<sup>th</sup> and 11<sup>th</sup> December 2011 at AITS, Rajampet.
37. One day work shop on “Latest Trends in ECE” on 19<sup>th</sup> September, 2011 at Siddharth group of Engineering Institutions, Puttur.
38. One week work shop (Wipro Mission10x) on “High Impact Teaching Skills and Cambridge Learning Approach” during 14 – 18 February, 2011 at AITS, Rajampet.
39. One day work shop on “Embedded Systems” on 22<sup>nd</sup> August, 2008 at CMR Group of Institutions, Hyderabad.
40. One Day work shop on “Embedded Systems Design with 89C51 Microcontroller” on 28<sup>th</sup> June, 2008 at Wine Yard Technologies, Hyderabad.
41. AICTE Sponsored seminar on “Embedded Systems and VLSI” on 21<sup>st</sup> June, 2008 at SVNEC, Rangampet, Chittoor Dist.
42. One week STTP on “Mechatronics” during 16 – 20 June, 2008 at Institute of Engineers India, Hyderabad Centre, Hyderabad.
43. One week AICTE and ISTE Sponsored STTP on “Advanced Signal Processing” during 5 – 9 May, 2008 at NBKRIST, Vidyanagar, Nellore Dist.

44. One day work shop on “MatLab” on 27<sup>th</sup> February, 2008 at GVPCOE, Visakhapatnam.
45. A Two day work shop on “Nanotechnology” on 7<sup>th</sup> and 8<sup>th</sup> December, 2007 at Nishita College of Engineering and Technology, Hyderabad.

***Professional Memberships:***

<b>S. No</b>	<b>Professional Society</b>	<b>Membership Number</b>
1	Institute of Electrical and Electronics Engineers (IEEE)	93289808
2	Indian Society for Technical Education (ISTE)	LM57062
3	International Association of Computer Science and Information Technology (IACSIT), Singapore	80342989
4	International Association of Engineers (IAENG), Hong Kong	116371
	Institute of Engineers, India (IEI)	AM099829-2

***Certifications***

1. MAVEN- SILICON - VLSI SoC Design using Verilog HDL
2. NPTEL – Switching Circuits and Logic Design – Topper (Top 1%)
3. NPTEL – Introduction to Internet of Things – Topper (Top 5%)
4. SWAYAM NPTEL- The Joy of Computing using Python (98%)
5. Coursera – Academic Information Seeking
6. Coursera – A Life of Happiness and Fulfillment
7. Coursera – Teacher SEL: Programs, Possibilities and Contexts
8. Coursera – Leadership and Emotional Intelligence
9. Coursera – Programming for everybody(getting started with python)
10. Coursera – Python Data Structures
11. edX – AI for everyone: Master the basics
12. edX – Success: Practical Thinking Skills
13. edX – Design Thinking Fundamentals

### *Any Other Achievements*

1. Attended as Jury member for project exhibition on 18-6-2022 at GNITS, Hyderabad.
2. Jury member for “NAVINA – An ideation contest” conducted by EEE department, BVRIT HYDERABAD CEW as part of the Engineer’s day celebrations on 12<sup>th</sup> September, 2020.
3. **Fellowship Award** for participating in the VLSID-2017 held during 7<sup>th</sup> – 11<sup>th</sup> January, 2017 at HICC, Hyderabad.
4. **Best presentation award** for presenting a paper entitled “FPGA implementation of a 64 point radix-2 single path delay feedback FFT architecture” at International Academic and Research Conference 2015 (IARC India 2015) in support of UNESCO’s 70<sup>th</sup> anniversary celebrations during 9-10, October, 2015, Vijayawada.
5. **Best paper award** in the International conference on Electrical, Electronics and Instrumentation Engineering (ICEEI-Aug’2015), during 20-22, August, 2015, Colombo, Sri Lanka for the paper entitled “Complexity Analysis of an 8 point FFT Processor for different Butterfly Structures”.
6. Awarded with “**Young Researcher Award**” for presenting a paper in National Conference on Emerging Trends in Information, Digital and Embedded Systems (NC’e-TIDES-15), on 28<sup>th</sup> Feb, 2015 at AITS, Rajampet.
7. Resource person for Faculty orientation program conducted at VITS, Hyderabad on 7<sup>th</sup> July, 2018.
8. Resource person in national technical symposium on “Emerging Trends in Engineering and Technologies” held at MRRITS, Udayagiri, on 22<sup>nd</sup> and 23<sup>rd</sup> of February, 2014.
9. Delivered a guest lecture on “Radio Communication Engineering (Theory)” at Andhra Pradesh Police Academy, HimayatSagar, Hyderabad on 11-11-13.
10. As a facilitator, trained junior faculty members of AITS on High impact teaching skills in the months of July and August, 2012.
11. Selection Committee Member of JNTUA interuniversity chess team selections for the years 2011-12 and 2012-13.
12. Appointed by JNTUA, Anantapur as subject expert to prepare question bank for the subject “Embedded and Real Time Systems”.
13. Qualified in **UGC-NET** for eligibility for Lectureship held on 24<sup>th</sup> June, 2012 in the subject Electronic Science.