

Personal Information

Name	Mrs. Rajidi Sahithi
Years of Experience	Teaching: 13 Years
Email Id	sahithi.r@bvrithyderabad.edu.in
Areas of Specialization	VLSI, Wireless Communications



Educational Qualifications

PG Degree	M.Tech.	VLSI System Design
UG Degree	B.E.	Electronics and Communication Engineering

Papers Published

International Journal Publications

1. V.Madhu Kumar, Dr.T.V.Ramana, Rajidi Sahithi, “User Content Delivery Service for Efficient POMA based LTE Channel Spectrum Scheduling Algorithm” in International Journal of Innovative Technology and Exploring Engineering (IJITEE) Page No 58-61, ISSN: 2278-3075, Volume-9 Issue-2S3, December 2019
2. Rajidi Sahithi, V.Madhu Kumar, Dr.T.V.Ramana, “Performance of WiMAX Physical layer MIMO-OFDM System under Rayleigh Fading Channel” in International Journal of Innovative Technology and Exploring Engineering (IJITEE) Page No 269-273, ISSN: 2278-3075, Volume-9 Issue-2S3, December 2019
3. Rajidi Sahithi, Dr.T.V.Ramana, “Inter-Carrier Interference Reduction in Broadband Wimax and LTE using Improved Firefly Method” in TEST Engineering Management ISSN: 0193-4120 Page No. 6423 - 6428 November-December 2019. (SCOPUS).
4. Tipparti Anil Kumar, Sk Nilofeer, Rajidi Sahithi, “Channel Estimation Techniques for OFDM and UFMC Systems for 5G Communications” in International Journal of Recent Technology and Engineering-IJRTE ISSN: 2277-3878- Volume 8, Issue- 2S8, Pages: 1776-1778, August 2019. Rajidi Sahithi, Dr.T.V.Ramana “Implementation and Analysis of MIMO-OFDM Systems with Bit Loading Algorithm and Fast Walsh-Hadamard STBC System for ICI Reduction” in International Journal of Recent Technology and Engineering-IJRTE - Volume 7, Issue- ICTESM, Pages: 175-181, March 2019, ISSN: 2277-3878.
5. Rajidi Sahithi, Dr.T.V.Ramana “Implementation Area Efficient Low Power OFDM Architecture” in International Journal of Advanced Research in Computer and Communication Engineering (IJARCCE)- volume 4 issue 7, Pages: 367-370, November 2015, ISSN 2278-1021
6. M.Manju, G Sahithi Reddy, “Optimizing the Modified Booth Recorder for Fused Add-Multiply Operator” in SSRG International Journal of VLSI & Signal Processing (SSRG-IJVSP) – volume 2 Issue 2 March-April 2015, ISSN: 2394 - 2584
7. K.Manoj, Rajidi Sahithi, K.Sruthi “The Development of Embedded and Embedded Linux Application in Intelligent Monitoring and Controlling System” in International

Conference on Communications, Signal Processing Computing and Information Technology (ICCSPCIT-14), Third Edition-2014, Pages:163-167 ISBN:978 93 83038 27 5

International Conference Publications

1. Paper on “Examination Room Guidance System” accepted in ICEAT 2022.
2. Presented Paper on “Student Team Achievement Division Type Collaborative Learning Model for Mastering Under graduate courses” in IUCEE Mini Technical Symposium-2022.
3. Rajidi Sahithi, Dr.T.V.Ramana “Implementation of Area Efficient OFDM Architecture” in International Conference of Signal Processing Computing and Communication-System Design (ICSPCOMSD)”- volume 2 August-2016, ISBN: 978-93-83038-42-8.

FDP's Attended / Conducted

1. 3 Day FDP on "4C's - THE NEED OF THE HOUR conducted by BVRITH from 8th June to 10th June 2022.
2. FDP on Integration of Renewable Energy and Smart Grids for Smart Cities conducted by Bansal Institute of Engineering and Technology Lucknow, Uttar Pradesh 7th March to 12 March 2022.
3. One day FDP on Moodle for Effective Course Management and Analytics conducted by VEDIC on 25th Jan 2022.
4. E&ICT FDP on Introduction to Low Power VLSI Design and Applications conducted by NIT Warangal from 23rd Dec 2021 to 1st Jan 2022.
5. Conducted One day Workshop on “Design Thinking” as a part of IIC BVRITH on 27th October 2021.
6. Atal FDP on Devices for Modern Wireless Communication System conducted by School of Studies in Engineering and Technology, Guru Ghasidas Vishwavidyalaya, Bilaspur from 20th to 24th September 2021.
7. Atal FDP on Internet of Things in 5G Wireless Communication conducted by North Eastern Regional Institute of Science and Technology (NERIST) from 6th to 10th September 2021.
8. Workshop on Embedded Systems-An application driven approach conducted by Arm Education, AICTE and STMICROELECTRONICS 25th August to 27th August 2021.
9. Academy Online Elementary FDP on "Challenges in adapting Machine Learning towards 5G/6G Communications (ATAL) conducted by Vallurupalli Nageswara Rao Vignana Jyothi Institute of Engineering and Technology from 9th August to 13th August 2021.
10. Online FDP on “Multi Technology” at BVRITH from 28th June to 3rd July 2021.
11. Attended a 2 Day Workshop on Arduino at CMRCET from 18th to 19th January 2020.
12. Attended the Seventh International Conference on Transformations in Engineering Education (ICTIEE-2020) as a delegate from 7th-9th Jan 2020, at CVSR-Anurag Group of Institutions.

13. Presented a Poster on “Monkey Repellent System” at Seventh International Conference on Transformations in Engineering Education (ICTIEE-2020) 7th-9th Jan 2020, at CVSR-Anurag Group of Institutions.
14. Attended a 5 Day FDP on Building Rural Resilience and WASH awareness and Volunteerism in association with MGNCRE at CMRCET from 17th to 21st December 2019.
15. Attended a Guest Lecture on Intellectual Property Rights on 28th September 2019, CMRCET.
16. Attended the First Annual International Conference on Engineering Education Research (AICEER-2019) from 9th-10th July 2019, CMRCET.
17. One-Day Workshop at PALLE SRUJANA on 17th July 2019
18. 2-Day Workshop on Rural Community Engagement and Rural Immersion under Unnat Bharat Abhiyan from 15th - 16th July 2019 in collaboration with Mahatma Gandhi National Council of Rural Education, jointly organized by Department of Higher Education, MHRD, Govt of India, CEER (CMRECT)
19. One-Week Faculty Development Programme on Machine Learning Applications in Signal Processing from 4th - 9th June 2019, In association with E & ICT Academy, NIT Warangal (NIT, Warangal)
20. One-Week Faculty Development Programme on Artificial Intelligence and Machine Learning from 29th March-4th April 2019, In association with E & ICT Academy, NIT Warangal (IARE-Dundigal, Hyderabad).
21. Participated in IUCEE-EPICS Orientation Workshop on 4th Feb 2019, CMRCET.
22. Attended the Sixth International Conference on Transformations in Engineering Education (ICTIEE-2019) as a delegate from 7th-8th Jan 2019, MREC.
23. One-Week Faculty Development Programme on Advances in VLSI Technology- Hands-on Experience from 17th-22nd Dec 2018,CMRCET.
24. Three Day Faculty Development Programme on Design the Thinking from 13th-15th October 2018,8012 FinTech Design Center, Siruseri, Chennai.
25. Workshop on MOOC DEVELOPMENT held at CMRCET, from 6th to 7th October 2018, conducted by Leadindia.ai a nationwide initiative by Bennett University, Greater Noida, India.
26. Two-Week Faculty Development Programme on Signals & Transform Techniques and their Applications from 21st-30th May 2018, In association with E & ICT Academy, NIT Warangal (CMRCET).
27. One week Faculty Development Programme on Advances in Wireless Communication Systems from 11th-15th December 2017 (CMRCET).
28. A Two-week ISTE STTP on CMOS, Mixed Signal and Radiofrequency VLSI Design In association with IIT Kharagpur from 30th January to 4th February 2017 (MRCET).
29. One week FDP on Application Oriented Teaching Methodologies from 15th to 19th November 2016 (MRECW).
30. One day Workshop on Research Trends in VLSI & Embedded Systems on 5th November 2016 under TEQIP-II (JNTU, HYDERABAD).
31. One week FDP on Effective Teaching Methodologies for Engineering Education from 1st to 5th November 2016 (MRECW).
32. A One-week Refresher Course on VLSI & EMBEDDED SYSTEMS from 22nd to 28th June 2016 (MRECW).

33. Two Day Workshop on VLSI DESIGN USING MENTOR GRAPHICS from 15th to 16th June 2016, In association with Core EL & Digilent Inc, USA. (MRCET).
34. Workshop on EMBEDDED LINUX ON ZYNQ-7000 SOC USING VIVADO on 25th September 2015, In association with Core EL & Digilent Inc, USA. (MRCET).
35. One Week Refresher course on DIGITAL SIGNAL PROCESSING (MRECW).
36. Two Week ISTE-STTP Main on INTRODUCTION TO DESIGN OF ALGORITHMS, In association with IIT Kharagpur from 25th to 30th May 2015 (MRCET).
37. A two-day FDP on VLSI Design Using Cadence Tools from 27th to 28th June 2014 (MRECW).
38. A ten-day FDP on VLSI Design Flow using Cadence tool (Geethanjali College of Engineering and Technology, Cheeryal, HYD).
39. FDP on TWO DAY HANDS-ON PROGRAM ON ANALOG SYSTEM DESIGN from December 12th to 13th 2012 (Anurag College of Engineering, Ghatkesar).
40. FDP on VLSI Design Flow using Synopsys EDA tool from 11th to 12th February 2013 (Seer Akademi USA Inc., Kukatpally, HYD).
41. FDP on ARM Cortex M3 Architecture from 2nd to 3rd July 2010. (SNIST, Ghatkesar, HYD).
42. A Three Day Faculty Development Programme from 22nd to 24th June 2011 (Geethanjali College of Engineering and Technology, Cheeryal, HYD).
43. VLSI DESIGN METHODOLOGIES AND TESTING PRINCIPLES from August 24th to 4th September 2009. (Vardhaman College of Engineering, Shamshabad).

Professional Memberships:

Member of IAENG with No: 223892

Certifications

- Completed Certification Course on Introduction to Programming with MATLAB. (Coursera -9 Weeks)
- Completed Certification Course on Introduction to Data Science in Python. (Coursera - 4 Weeks)
- Completed Certification Course on What is Data Science?. (Coursera - 3 Weeks)
- Completed Certification Course on Wireless Communications for Everybody. (Coursera - 6 Weeks)
- Completed IIEECP (IUCEE International Engineering Educators Certification Program) with Distinction in the academic Year 2019-20.
- An 8-week course on Hardware Modelling Using Verilog from August- October 2019 by IIT Kharagpur (NPTEL Course).
- Completed Phase-1 Pre-Certification Workshop of IUCEE-IIEECP 2019 conducted from 1st -3rd April 2019 at CMRCET.
- Completed a webinar-based course on DESIGN THINKING 2019 - Design Thinking and Community-Based Design presented by EPICS Program Purdue University in the academic year 2018-19.

- A 12-week course on Principles of Signal Estimation for MIMO/OFDM Wireless Communication from July-October 2018 by IIT Kanpur (NPTEL Course) (equivalent to 1.5 weeks FDP).
- An 8-week course on Estimation For Wireless Communications: MIMO-OFDM Cellular and Sensor Networks from July-September 2017 by IIT Kanpur (NPTEL Course).