

Name of the Activity: What's my audibility?

Faculty Name: Dr. J. Naga Vishnu Vardhan
Class / Semester: IV ECE (A) / I Sem
Academic Year: 2017-18
Subject Name: Cellular and Mobile Communications
Topic: Calculate MOS (Mean Opinion Score) in the Campus

Brief Write-up (Not exceeding 200 Words)

Mean Opinion Score is defined as the provides a numerical measure of the quality of human speech at the destination end of the circuit. The scheme uses subjective tests (opinionated scores) that are mathematically averaged to obtain a quantitative indicator of the system performance.

To Experience the students about MOS, they are asked to form a group of 4 to 5 and every group is assigned a particular cellular network like Idea, Vodafone, BSNL, Airtel etc and asked to reach every faculty and get Score of 1 to 5 about the network used by them in the campus in their sitting place. They are finally asked to tabulate the score given by faculty for a particular network and asked to take average to find MOS. If Score is in between 4 to 5 It is Excellent, 3 to 4 it is Very Good, 2 to 3 - Average Below 2 - Poor.

Objective:

To make the student understand the concept of Mean Opinion Score by which the signal strength of a particular network can be analyzed

No. of Teams: 5

Preparation / Prerequisites:

Should know the Concept of Mean Opinion Score and its importance in Cellular Communications

S. No	Faculty	Network Used	Score	Block
1	1	Idea	5	Opal
2	2	Idea	4	Opal
3	3	Idea	5	Pearl
4	4	Idea	4	Pearl
5	5	Idea	3	Diamond
6	6	Idea	5	Opal
7	7	Idea	4	Pearl
8	8	Idea	4	Pearl
9	9	Idea	3	Diamond
10	10	Idea	5	Pearl
Mean Opinion Score			4.2	

If Score is in between 4 to 5 It is Excellent, 3 to 4 it is Very Good, 2 to 3 – Average Below 2 – Poor. Hence as MOS for Idea Network in the BVRITH Campus is 4.2, it is rated as Excellent.

For any queries, please contact: vishnu.j @bvrithyderabad.edu.in



Name of the Activity: Crossword Puzzle

Faculty Name: Mr. R.Priyakanth Class / Semester: II - I Academic Year : 2017-18 Subject Name: Signals and Stochastic Processes Topic: Laplace & Z- Transforms, Random Processes – Temporal & Spectral Characteristics

Brief Write-up (Not exceeding 200 Words)

This is a Crossword puzzle based activity wherein students in teams will participate. Team size is 2. This competition consists of an empty crossword and clues for horizontal and vertical. Students should answer the crossword based on the clues given. This is a time based activity cum competition and the duration is 30 min. Students whoever completes this crossword correctly with more points in less time will be the winners.

Objective:

To use crossword puzzle as a teaching tool in Signals and Stochastic Processes and evaluate students' perception about the same.

Photographs

Crossword Puzzle and Key





For any queries, please contact to below mail

priyakanth.r@bvrithyderabad.edu.in



Name of the Activity: Student Seminars

Faculty Name: G Siva Sankar Varma Class / Semester: II ECE (A) / I Sem Academic Year: 2017-18 Subject Name: Analog Electronics Topic: Unit- I of AE – Exact Hybrid Analysis of CE Configuration and Exact Hybrid Analysis of CC Configuration

Brief Write-up (Not exceeding 200 Words):

Students presented seminars on topics: Exact Hybrid Analysis of CE Configuration and Exact Hybrid Analysis of CC, of AE Unit 1.

SL NO	ΤΟΡΙϹ	DATE	ROLL NO	NAME OF THE STUDENT
1	Exact Hybrid Analysis of CE Configuration	04/09/2017	16WH1A0402	A CHARISHMA
2	Exact Hybrid Analysis of CC Configuration	04/09/2017	16WH1A0429	K S MOUNIKA

Objective:

To improve communication skills, gaining expert knowledge, motivation and confidence.

Photographs:



A CHARISHMA



K S MOUNIKA

For any queries, please contact to below mail

sivasankar.g@bvrithyderabad.edu.in



Name of the Activity: Chart Preparation

Faculty Name: G Siva Sankar Varma Class / Semester: II ECE (A) / I Sem Academic Year: 2017-18 Subject Name: Analog Electronics Topic: Unit- I to V of AE

Brief Write-up (Not exceeding 200 Words):

Chart Preparation activity was held regarding Different transistor amplifiers and their applications the students were divided into 14 groups and 58 students participated:

Objective:

Students will learn how to communicate within a team/group and develop innovative thinking on various applications of amplifiers to prepare the chart.

Photographs:









For any queries, please contact to below mail

sivasankar.g@bvrithyderabad.edu.in



Name of the Activity: Student Seminars

Faculty Name: G Siva Sankar Varma
Class / Semester: III ECE (A) / II Sem
Academic Year: 2017-18
Subject Name: Microprocessors and microcontrollers
Topic: Unit- I of MPMC – Memory Organisation, Flag registers, Minimum mode read and write cycles of 8086

Brief Write-up (Not exceeding 200 Words):

Students presented seminars on Memory Organisation, Flag registers, Minimum mode read and write cycles of 8086 from Unit 1 of MPMC.

SL NO	ΤΟΡΙϹ	ROLL NO	NAME OF THE STUDENT
1	Memory Organisation	15WH1A0413	B LAVANYA
2	Flag Registers	15WH1A0440	D TWINKLE
3	Read cycle of Minimum Mode operation	15WH1A0447	V SAHANA SRI
4	Write cycle of Minimum Mode operation	16WH5A0405	AKHTHAR BAWAZEER

Objective:

To improve communication skills, gaining expert knowledge, motivation and confidence.

Photographs:





AKHTHAR BAWAZEER

B LAVANYA



V SAHANA SRI

D TWINKLE

For any queries, please contact to below mail

sivasankar.g@bvrithyderabad.edu.in



Name of the Activity: Guess the word

Faculty Name: Ms. R.Shylaja Class / Semester: II EEE / II SEM Academic Year: 2017-2018 Subject Name: Switching Theory & Logic Design Topic: Summary of the Subject

Brief Write-up (Not exceeding 200 Words)

Total class is divided in to 4 teams. One of the students from a team should guess the word related to the subject from the clues given by her team members. Word to be guessed is given by other team members.

Example: Register Clues

- 1. Group of flip flops
- 2. used to store binary numbers
- 3. attendance will be noted

Words given by other teams

- Sequential circuit
- Flip-flop
- Counter
- Mealy machine
- FSM
- Sequence detector
- Parity generator
- Kmap
- combinational circuit
- State table
- Gate
- Multiplexer
- Decoder

Objective:

After this activity and associated work students are expected to be able to

- 1. List the Key topics they learnt
- 2. Recollect the concepts to give clues

Photographs:



For any queries, please contact to below mail

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