


Personal Information

Name	Ms. Manju Padidela	
Years of Experience	Teaching: 9 Years	
Email Id	manju.p@bvrithyderabad.edu.in	
Areas of Specialization	VLSI	

Educational Qualifications

PG Degree	MS(USA)	VLSI
UG Degree	B.Tech.	Electronics and Communication Engineering

Patent Filed

Filed a Provisional patent on “Support Vector Machine for Speech Recognition”, Intellectual property India, Application No: 20204102603

Papers Published

International Journal Publications

1. P.Manju, N.Mouni, Dr.T. Madhavi, “Design and Implementation of Parallel Array Multiplier Using High Speed Adders”, International Journal of Advanced Science and Technology, ISSN: 2207-6360, Vol. 29, No. 6, (2020), pp. 2912 - 2923 . [**SCOPUS**]
2. P.Manju, N.Mouni, Dr.T. Madhavi, “Design and Implementation of High Speed Adders Using the GDI and CMOS Technology”, TEST Engineering and Management, ISSN: 0193-4120 Page No. 2625 – 2630, May – June 2020 . [**SCOPUS**]
3. P. Manju, Dr. T Madhavi, “Design of A High Speed Hybrid Transistor Logic (HTL) Multiplier Using HTL Adder”, Ponte International Journal of Sciences and Research, ISSN: 0032-423X, Vol. 75, No. 11/1, Nov 2019. [**Web of Science**]
4. P. Manju, Dr. T Madhavi, “Design of A High Speed Hybrid Transistor Logic (HTL) based Cryptography”, International Journal of Advanced Science and Technology, ISSN: 2207-6360, Vol. 28, No. 14, 2019, pp. 181-190. [**SCOPUS**]
5. P. Manju, Dr. T Madhavi, “Design of A High Speed Hybrid Transistor Logic (HTL) based Cryptography”, International Journal of Advanced Science and Technology, ISSN :2207-6360, Vol. 28, No. 14, 2019, pp. 181-190. [**SCOPUS**]
6. P. Manju, P. B. Sreelakshmi, Dr.T. Madhavi, “Design of Vedic Multiplier using GDI Method”, in International Journal of Recent Technology and Engineering (IJRTE), Vol. 7, Issue 6S4, April 2019, ISSN: 2277-3878 [**SCOPUS**].
7. P. Manju, Dr. T. Madhavi, P. B. Sreelakshmi, “Design of A High Speed Novel Adder Using Hybrid Transistor Logic”, in Journal of Advanced Research in Dynamical & Control Systems, Vol. 10, 04-Special Issue, 2018, ISSN: 1943-023X [**SCOPUS**].

8. P. Manju, Dr. T. Madhavi, "Reversible Logic to Cryptographic Hardware using Montgomery Modular Multiplication Algorithm", International Journal of Computer Applications -IJCA, 2017, ISSN: 0975-8887.
9. P. Manju, Dr. T. Madhavi, "Implementation of Area Efficient and Low Power Adder Circuits Using Transmission Gates", in International Journal of Engineering Sciences Research-IJESR, Vol 07, Special Issue, January 2016, ISSN: 2230-8504.
10. D. Prasad, P. Manju, "TDOA Computation Using Multicarrier Modulation in Sensor Networks with Efficient Bandwidth", in International Journal of Advanced Trends in Computer Science and Engineering, Vol.2 , No.1, Pages : 413-418, 2013 ISSN 2278-3091.
11. P. Manju, G. Sahithi Reddy, "Optimizing the Modified Booth Recoder for Fused Add-Multiply Operator", in SSRG International Journal of VLSI & Signal Processing (SSRG-IJVSP) – volume 2, Issue 2, March-April 2015, ISSN: 2394 – 2584.
12. M. Aruna Jyothi, P. Manju, "Development of Activation of Industrial Applications using ARM7", in International Journal & Magazine of Engineering, Technology, Management and Research (IJMETMR) – volume 2, issue 7, July 2015, ISSN: 2348 – 4845.
13. P. Manju, "Forecasting Bus Arrival Time With Mobile Phone Based Participatory Sensing System", in International Journal of Eminent Engineering Technologies (IJOEET) – volume 3, issue 2, July 2015, ISSN: 2448 – 2845.

International Conference Publications

1. Presented Paper on "Student Team Achievement Division Type Collaborative Learning Model for Mastering Under graduate courses" in IUCEE Mini Technical Symposium-2022.
2. P. Manju, P. B. Sreelakshmi, Dr.T. Madhavi, "Design and Comparison of various VLSI adders using GDI method", in International Conference on Electrical, Communication, Electronics, Instrumentation and Computing organized by IEEE Madras Section, Paper Code: ICECEIC-0454, Catalog: efp19R88-PRJ: 978-1-7281-0173-6, Jan 2019.

National Conference Publications:

1. P. Manju, Dr. T. Madhavi, "Reversible logic to Cryptographic Hardware using Montgomery Modular Multiplication Algorithm", National Conference on Advances in Communication Technologies NACT 2017.

FDP's Attended / Conducted

1. FDP on Integration of Renewable Energy and Smart Grids for Smart Cities conducted by Bansal Institute of Engineering and Technology Lucknow, Uttar Pradesh 7th March to 12th March 2022.
2. E&ICT FDP on Introduction to Low Power VLSI Design and Applications conducted by NIT Warangal from 23rd Dec 2021 to 1st Jan 2022.
3. A one-day webinar on 360-degree view on research management & academic journal publishing, conducted by ICMR on 12th November 2021.

4. A 5 day online FDP on Artificial Intelligence AI enabled Translational Research in Health Informatics, Manakula Vinayagar Institute of Technology with JIPMER, Puducherry, from 25th October 2021 to 29th October 2021.
5. A one-day FDP on Patent Law for Engineers by KLIPFC, CIIE on 30-01-2021
6. A one-week FDP on Wireless Sensor Networks by GZSCCET MRSPTU, Bathinda from 18-01-2021 to 23-01-2021
7. A five-day FDP on Robotics by K.E. Society's Rajarambapu Institute of Technology, Rajaramnagar, ATAL from 07-12-2020 to 11-12-2020
8. A one-day FDP on Awareness Webinar on Outcome based Education and Accreditation (OB&E) by National Board of Accreditation, New Delhi on 25-11-2020
9. A one-day FDP on Innovations in Electrical and Electronics Engineering by IIMT College of Engineering and Technology, Greater Noida on 07-11-2020
10. A nine-day FDP on National IP Literacy Week by MHRD from 15-10-2020 to 23-10-2020
11. A five-day FDP on Artificial Intelligence by Acropolis Institute of Technology and Research, ATAL from 02-09-2020 to 06-09-2020
12. A three-day FDP on Research Applications in Artificial Intelligence, Data Science and Emerging Technologies by Bharat Institute of Engineering and Technology from 17-07-2020 to 19-07-2020
13. A five-day FDP on Artificial Intelligence and Machine Learning by CMR Technical Campus from 18-05-2020 to 22-05-2020
14. A one-day FDP on India First Leadership Talk by MHRD on 30-05-2020
15. A one-day FDP on India First Leadership Talk by MHRD on 16-05-2020
16. A one-day FDP on Knowledge Based Innovative MSME Schemes for Academic Institutions by IEEE Photonics, Mangalam College, Kerala on 16-05-2020
17. A one-day FDP on Handling Post Covid & How to Adjust to it by IIMT College of Management, Greater Noida on 15-5-2020
18. A one-day FDP on India First Leadership Talk by MHRD on 09-05-2020
19. A one-day FDP on Dangers of Data by IEEE Photonics, Mangalam College, Kerala on 6-5-2020
20. A one- week ICT Mode Short Term Training Programme (STTP) on Indian Electricity Rule And Code Of Practices conducted from 16-12-2019 to 20-12-2019 organized by NITTTR, Kolkata.
21. A one- week ICT Mode Short Term Training Programme (STTP) on Laboratory Safety Management conducted from 23-09-2019 to 29-09-2019 organized by NITTTR, Kolkata.
22. A two-day FDP on VLSI Design Using Cadence Tools (Malla Reddy Engineering College for Women, Dhulapally)
23. A two-week ISTE workshop on Signals & Systems (Joginpally B.R.Engineering College) conducted by Indian Institute of Technology (IIT) Kharagpur.
24. A two-week ISTE workshop on Introduction to Research Methodologies (Joginpally B.R.Engineering College) conducted by Indian Institute of Technology (IIT) Bombay.
25. A one-day FDP on Total Quality Teacher (Lords Institute of Engineering & Technology) in association with Association of Indian Management Schools (AIMS).
26. A five-day refresher course on Digital Signal Processing (Malla Reddy Engineering College for Women, Dhulapally)

27. A two-week ISTE workshop on Design of Algorithms (Malla Reddy College of Engineering and Technology) conducted by Indian Institute of Technology (IIT) Kharagpur.
28. A two-week ISTE workshop on CMOS, Mixed Signal and Radio Frequency VLSI Design (Malla Reddy College of Engineering and Technology) conducted by Indian Institute of Technology (IIT) Kharagpur.

Professional Memberships:

Member of IAENG with No: 251371

Certifications

- Completed Certification Course on AI for Everyone. (Coursera -4 Weeks)
- Completed Certification Course on FPGA Computing Systems: Background Knowledge and Introductory Materials. (Coursera - 4 Weeks)
- Completed Certification Course on Fundamentals of Network Communication. (Coursera - 5 Weeks)
- Completed Certification Course on Wireless Communications for Everybody. (Coursera - 6 Weeks)
- Completed Certification Course on Introduction to Electronics. (Coursera - 7 Weeks)

Any Other Achievements:

- Secured A.P state 8th rank in BIE in the year 2003.
- Received appreciation for scoring highest feedback percentage in Probability Theory and Stochastic Processes for II BTech I Sem 2021-22