

### **Personal Information**

Name	Dr. M. Parvathi
Years of Experience	Teaching: 21 Years Research: 6 years
Email Id	parvathi.m@bvrithyderabad.edu.in
Areas of Specialization	VLSI Design, and Embedded Systems



### **Educational Qualifications**

Doctoral Degree	Ph.D.	ECE-VLSI Design (Embedded Memory Testing)
PG Degree	M.Tech.	Digital Systems & Computer Electronics
UG Degree	B.Tech.	Electronics & Communication Engineering

### **Patent Published:**

1. “AI Enabled Framework for Early Diagnosis Of Autism Spectrum Disorder (ASD)” - The Patent Office Journal No. 11/2021 Dated 12/03/2021, Application No.202141008949 A, Publication Date: 12/03/2021, Pp: 12850-12851.
2. “An AI Enabled Method for Parasitic R & C Estimation in Progressive Stacked SRAM Circuits” - The Patent Office Journal No. 17/2021 Dated 23/04/2021, Application No.202141016526 A, Publication Date: 23/04/2021.

### **Papers Published**

#### **International Journal Publications**

1. **M. Parvathi**, Ch. Manaswini, “Prediction Analysis of Cancer Cells Using ML Classification Algorithms”, Indian Journal of Public Health Research & Development, April-June 2021, Vol. 12, No. 2, Pp: 184-189.
2. **M.Parvathi**, M. Ravikanth, and M.Neelakantappa, “Early Detection Support Mechanism in ASD using ML Classifier”, Turkish Journal of Computer and Mathematics Education, Vol.12 No.10 (2021), Pp: 4543-4549, Research Article
3. **M. Parvathi**, “Activity Based Analysis and Prediction Strategy for the Class Room Performance Improvement”, Journal of Engineering Education Transformations, (**SCOPUS**) Volume 34, January 2021, Special issue, eISSN 2394-1707, Pp:686-693.
4. **Parvathi M.** (2020) High-Accurate, Area-Efficient Approximate Multiplier for Error-Tolerant Applications. In: Pant M., Kumar Sharma T., Arya R., Sahana B., Zolfagharinia H. (eds) Soft Computing: Theories and Applications. Advances in Intelligent Systems and Computing (SOCTA2020), (**SCOPUS**), vol: 1154, Springer, [https://doi.org/10.1007/978-981-15-4032-5\\_10](https://doi.org/10.1007/978-981-15-4032-5_10).
5. **M.Parvathi**, “Two Cell Fault Models and Parasitic RC Test Method for Embedded SRAM”, International Journal of Engineering & Technology (UAE), (**SCOPUS**) Vol. 7, No 4.29 (2018) , Pp: 235-238, DOI: 10.14419/ijet.v7i4.29.26262., ISSN: 2227-524X.

6. **M.Parvathi**, N.Vasanth, K.Satya Prasad, “BIST Architecture using Area Efficient Low Current LFSR for Embedded Memory Testing Applications”, International Journal of Reconfigurable and Embedded Systems, Vol. 7, No. 1, March- 2018, pp.1~11, ISSN: 2089-4864, DOI: 0.11591/ijres.v7.i1.Pp1-11.
7. **M.Parvathi**, “Designs, Implementation Methods and analysis for SRAM”, International Journal of Current Engineering and Scientific Research, pISSN: 2393-8374, eISSN: 2394-0697, Vol-5, Issue-1-Part-III, Jan-2018, Peer reviewed, IF: 3.241, Pp: 20-27.
8. Swarna Uma, **M Parvathi**, “BCD Adder Design using new Reversible Logic for Low Power Applications”, Indian Journal of Science and Technology, Vol 10(30), DOI: 10.17485/IJST/2017/v10i30/115514, August 2017, ISSN (Print) : 0974-6846, ISSN (Online) : 0974-5645, Pp:1-7.
9. **M.Parvathi**, N.Vasanth, K.Satya Prasad, “Fault Model Analysis by Parasitic Extraction Method for Embedded SRAMs”, IJRET: International Journal of Research in Engineering and Technology Volume: 02 Issue: 12, Dec-2013 eISSN: 2319-1163 | pISSN: 2321-7308, Peer Reviewed and refereed with IF 1.962, Pp: 501-509.
10. **M.Parvathi**, N.Vasanth, K.Satya Prasad, “Design of High Speed Low Power High Accurate (HS-LP-HA) Adder”, International Journal of Computer and Communication Engineering (IJCCE), ISSN: 2010-3743, Vol: 2, Issue 5, September-2013, Peer Reviewed and refereed with IF 1, Pp: 547-550.
11. **Muddapu Parvathi**, N.Vasanth, K.Satya Prasad, “Modified March C- with Concurrency in Testing, For Embedded Memory Applications”, International Journal of Electrical and Computer Engineering (IJECE), Vol. 2, No.5, October 2012, ISSN: 2088-8708, IAES Publisher, Peer Reviewed and refereed with IF 3.02, DOI: <http://dx.doi.org/10.11591/ijece.v2i5.1587>, Pp. 571-576.
12. Shilpa Darvesh, **M.Parvathi**, “1-Bit Nano CMOS Full-Adder cell for Energy-Efficient Arithmetic Applications”, International Journal of Electronics and Communication Engineering (IJECE) ISSN 0974-2166 Volume 5, Number 3 (2012), Peer Reviewed and refereed with IF 3, Pp. 181-187.
13. Shilpa Darvesh, **M.Parvathi**, “1-Bit Full-Adder cell with Optimized Delay for Energy- Efficient Arithmetic Applications”, International Journal of Electronic Networks, Devices and Fields (IJENDF) ISSN 0974-2182 Volume 4, Number 1 (2012), , Peer Reviewed and refereed with IF 3, Pp. 1- 7.

### *International Conference Publications*

1. **Parvathi, M.**, Pattan, A.B. (2022). ML-Based Comparative Analysis of Interconnect RC Estimation in Progressive Stacked Circuits. In: Suma, V., Fernando, X., Du, KL. Wang, H. (eds) Evolutionary Computing and Mobile Sustainable Networks. Lecture Notes on Data Engineering and Communications Technologies, vol 116. Springer, Singapore. [https://doi.org/10.1007/978-981-16-9605-3\\_73](https://doi.org/10.1007/978-981-16-9605-3_73). (SCOPUS)
2. Venkatesham Maddela, Dr. Sanjeet K Sinha and **Dr.Parvathi Muddapu**, “Study on Paradigm of Variable Length SRAM Embedded Memory Testing”, Proceedings of the Fifth International Conference on Electronics, Communication and Aerospace Technology

- (ICECA 2021), ISBN: 978-1-6654-3523-9, 978-1-6654-3524-6/21/\$31.00 ©2021 IEEE, Pp:129-133.
3. Venkatesham Maddela, Dr. Sanjeet K Sinha and **Dr.Parvathi Muddapu**, “Extraction of undetectable Faults in 6T-SRAM Cell”, In proceedings of International Conference on Communication, Control and Information Sciences (ICCISc-2021), Organized by Government Engineering College, Idukki Painavu, Kerala, in collaboration with IEEE-Kerala Section, 2021, Pp: 13-17. (**SCOPUS**)
  4. Venkatesham Maddela, Dr. Sanjeet K Sinha and **Dr.Parvathi Muddapu**, “Open Defect Fault Analysis in Single Cell SRAM Using R, and C Parasitic Extraction”, International Conference on Information & Communication Engineering(ICICE-2021), February 11 - 13, 2021, ISBN: 978-93-5437-185-1, Pp:30-31.
  5. **Dr.Parvathi M**, Ravikanth M and Neelakantappa M, “Early Detection Support Mechanism in Autism Spectrum Disorder using ML Classifier”, Proceedings Of International Conference On Information & Communication Engineering, ISBN: 978-93-5437-185-1, 2021, Pp: 34-35.
  6. **Parvathi. Muddapu**, "Camera and Biometric based Vehicle Monitoring System for Public Safety", IEEE-HYDCON, 2020, pp. 1-6, doi: 10.1109/HYDCON48903.2020.9242840.
  7. **M. Parvathi**, B. Himasree, T. Bhavyasree, “Novel Test Methods for NPSF Faults in SRAM”, Proceedings in **IEEE International Conference On Computational and Characterization Techniques in Engineering & Sciences (CCTES-18)** September 14-15, 2018, Organized by Department of ECE, EE & Physics, INTEGRAL UNIVERSITY, LUCKNOW, P-35. DOI: 10.1109/CCTES.2018.8674087. (**SCOPUS**)
  8. **M. Parvathi**, “New March Elements for Faults due to Open Defects in eSRAM” Proceedings in First International Conference on Digital Contents and Applications (DCA 2018), Sydney, Australia, January 10 - 12, 2018, ISBN 978-81-933584-3-6, Vol:1,P-06.
  9. **M.Parvathi**, K.Satya Prasad, N.Vasantha, “Testing of Embedded SRAMs using Parasitic Extraction Method”, Proceedings in Robotic, Vision, Signal Processing and Power Applications (ROVISP), Empowering Research and Innovation, Editors: Ibrahim, H., Iqbal, S., Teoh, S.S., Mustafa, M.T. (Eds.), ISBN 978-981-10-1721-6, Jan-2017, **Springer LNEE**, Pp:47-61.(**SCOPUS**)
  10. **M.Parvathi**, N.Vasantha, K.Satya Prasad, “New Fault Model Analysis for Embedded SRAM Cell for Deep Submicron Technologies using Parasitic Extraction Method”, Proceedings in 2015 **IEEE conference on VLSI Systems, Architecture, Technology and Applications (VLSI-SATA)** organized by Amritha Vishwa Vidyapeetham (University), School of Engineering, Bengaluru Campus, ISBN: 978-1-4799-7925-7, 9781479979271, Pp:1-6. **IEEE Publications**, DOI: 10.1109/VLSI-SATA.2015.7050471.(**SCOPUS**)
  11. Ramakrishna Reddy, A, **Parvathi, M.** “Efficient carry select adder using 0.12µm technology for low power applications”, Proceedings in International Conference on Advances in Computing, Communications and Informatics (ICACCI), 2013 DOI: 10.1109/ICACCI.2013.6637231, Publication Year: 2013, **IEEE Conference Publications** ISBN: 978-1-4799-2432-5, Pp: 550 – 553.(**SCOPUS**)

12. **M.Parvathi**, N.Vasantha, K.Satya Prasad, “BIST Controller With Efficient Decoder and Adder for High Speed Embedded Memory Applications”, Proceedings in Asia Pacific Conference on Postgraduate Research in Microelectronics & Electronics (**IEEE Prime Asia-2012**),ISBN:978-1-4673-5065-5,Pp:234-239, (**SCOPUS**)

### *National Conference Publications*

1. **M. Parvathi**, “Designs, Implementation Methods and Comparisons for SRAM”, Proceedings in National Conference on Advanced Signal Processing, Embedded and Communication Systems (ASPECS- 2016) 11<sup>th</sup> – 12<sup>th</sup> August 2016 Jointly organized by Dept. of ECE, CBIT and Research Centre Imarat, **DRDO Supported by TEQIP**, in technical association with IETE Hyderabad Centre ISTE Chapter of CBIT.
2. N.Vasantha, **M. Parvathi**, “High speed BIST Controller for low power applications”, Proceedings in National Conference on Circuits Signals and Systems – 2015 (NCCSS 2015 January 22 – 24, 2015 ),
3. G.Mrudula, **M.Parvathi**, “A Palm Print Recognition System based on DM3730”, Proceedings in National Conference of Advanced Communication Trends ACT’12, 2012, pp: 173-176.

### *Books & Book Chapter*

Authored a Book on “Testing of Embedded SRAM Using Modified March C- (MMC-) Algorithm and Parasitic Extraction Method”, published by Lambert Academic Publishing, Germany, 2017.

### *FDP’s Conducted*

1. Organized One Week Online AICTE –ATAL Sponsored Faculty Development Programme on “Machine Learning Applications in Micro-Nano VLSI Technologies” at BVRIT HYDERABAD College of Engineering for Women, Bachupally, Hyderabad from 23<sup>th</sup> to 30<sup>th</sup> August 2021.
2. Two-day FDP on “Research and Ethics”, by Dr.Abdul Solman Moiz, Professor at UOH, organized on behalf of R&D, BVRITH in collaboration with IEEE Hyderabad Section & GRSS, on 28th & 29th May 2020.

### *FDP’s Attended*

1. **Three day FDP** on “4 C's(Critical Thinking, Creativity Collaborating, Communication)  
Organized by BVRIT HYDERABAD College of Engineering for Women, 8<sup>th</sup> – 10<sup>th</sup> June 2022.
2. **One week FDP** on “Smart Autonomous Driving Systems” held at Ramaiah Institute of Technology, Bangalore, 7<sup>TH</sup> to 11<sup>TH</sup> Mar 2022
3. **One-week Online STTP** on "Covid-19 Data Analysis with Deep Learning", jointly organized by AICTE, New Delhi and Human Resource Development Centre, Jawaharlal Nehru Technological University Hyderabad,Kukatpally, Hyderabad,T.S., India, 8<sup>th</sup> to 15<sup>th</sup> Nov 2021
4. **One Week Faculty Development Program** on “Machine Learning Fundamentals“ organized by BVRIT HYDERABAD College of Engineering for Women in

- association with Institutions Innovation Council , Tech Blitz and Departments of CSE (AI&ML), CSE & IT, BVRITH, 30<sup>th</sup> Aug to 4<sup>th</sup> Sep 2021
5. **One week AICTE (ATAL) FDP** on “Artificial intelligence in Electronic Design Automation” by GITAM Deemed to be University, 9<sup>th</sup> to 13<sup>th</sup> Aug 2021
  6. **Five Day FDP** on “Digital VLSI Design and Verification” organized by Department of ECE, Bangalore Institute of Technology in association with Entuple Technologies and IEEE BIT CAS. 23<sup>rd</sup> to 27<sup>th</sup> Aug 2021
  7. **One week AICTE (ATAL) FDP** on "Machine Learning Techniques In VLSI Design”, MS Ramaiah Institute Of Technology. 26<sup>th</sup> to 30<sup>th</sup> July 2021.
  8. **Online orientation Training Program** for Mentors by NITTTR National initiative Technical Teachers Training, Chennai, 26<sup>th</sup> to 30<sup>th</sup> July 2021.
  9. **One Week online Faculty Development** Program on “Artificial Intelligence”, organized by Bansal Institute Of Engineering And Technology. Lucknow (U.P.) India, 19<sup>th</sup> to 25<sup>th</sup> July 2021.
  10. **One Week National Level Online Faculty Development** Program on “Multi Technology” from 28th June to 03rd July 2021 organized by Department of CSE & IT in association with NYCI & Brainovision Solutions India Pvt. Ltd, 19<sup>th</sup> to 25<sup>th</sup> July 2021
  11. **Five Day Faculty Development Programme** on “**How to Write Good Research Article & Proposal**”, organized by Department of EEE, ECE, CSE & IT, BVRIT HYDERABAD College of Engineering for Women, **18<sup>th</sup> to 22<sup>nd</sup> Jan 2021**
  12. **One week FDP** on "Internet of Things (IoT)" organized by National Institute of Technology, Durgapur, under AICTE Training And Learning (ATAL) Academy Program, 5th – 9th Jan 2021
  13. AICTE Sponsored **STTP** on “Artificial Intelligence and Machine Learning with Data Science”, organized by the Department of Information Technology Guru Nanak Dev Engineering College, Ludhiana, 14th to 19th Dec2020.
  14. **One week AICTE (ATAL) FDP** on "Biomedical Instrumentation", University B D T College Of Engineering Davanagere, 7<sup>th</sup> to 11<sup>th</sup> Dec 2020.
  15. **One week FDP** on “Augmented Reality (AR)/ Virtual Reality (VR)", organized by Guru Nanak Dev Engineering College, Ludhiana, under AICTE Training And Learning (ATAL) Academy Program, 23rd -27th Nov, 2020
  16. **One Week FDP** on “DATA SCIENCE” organized by Department of Computer Science & Engineering, Rajarambapu Institute of Technology Rajaramnagar under AICTE Training And Learning (ATAL) Academy, 16th -20th Sep 2020
  17. **Half week FDP** on Introduction to Embedded System Design by NPTEL, Sep-Dec 2020
  18. **Two days online FDP** on “Fiddle with Virtual Components”, Organized by Depat. of ECE, BVRITH, 4<sup>th</sup> to 5<sup>th</sup> Sep 2020
  19. **One Week Online STTP** on "Learning Management System: Moodle”, Center for Educational Technology, NITW, 10th -14th Aug, 2020
  20. **One Week Faculty Development Program** on "Advancements and Research Opportunities in Electronics & Telecommunications", organized by E&TC Department of Bharati Vidyapeeth Deemed to be University College of Engineering, Pune in association with S. B. Patil College of Engineering- Indupur, 04th to 08th August 2020
  21. **One week online FDP** on "Current Research Trends in ECE", By RGM CET, 29th to

3rd July 2020

22. **FDP** - six day webinar series, on "Applications of ML", Organized by **MGIT**, 20th to 25th July 2020
23. **Five day FDP** on "Wireless Communications and Mobile Technologies" held at BVRIT HYDERABAD College of Engineering for Women, 06th to 10th July 2020
24. **One week Online FDP** on "Advanced Data Science and Its Applications", **CSE Dept., BVRITH**, 22nd – 26th Jun 2020
25. **Short term training program (STTP)** on “An Emerging Paradigm of low power computational VLSI Design”, by **BVRITN**, 16<sup>th</sup> to 21<sup>st</sup> June 2020
26. **One week FDP** on ‘Possessing the Two Wardrobes: Communication & Writing’, by **H&S, BVRITH**, 25<sup>th</sup> to 31<sup>st</sup> May 2020
27. **One-week FDP** on “Artificial Intelligence”, organized by Dept. of **CSE, BVRITH**, 22nd to 26th May 2020
28. **One Week FDP** on Teaching Learning and Assessment Strategies, IEEE Hyderabad section, 18th -23rd May 2020
29. **FDP** on “VLSI Chip Design using open source EDA”, organized by Electronics & **ICT Academy** supported by Ministry of Electronics and Information Technology (Meit Y), Govt. of India, **IIT Guwahati**, at ECE Dept. **VNR VJIET**, Hyderabad, 8th -12th July 2019
30. **Three days FDP** on Professional Etiquette & Emotional Intelligence organized by HS Dept, **BVRITH**, 8th to 10th July, 2019
31. 1&1/2 week FDP in Programming, Data Structures And Algorithms Using Python – NPTE, July-Oct 2019
32. **Week day FDP** in Micro Electronics: Device to Circuits-NPTEL, 18th -19th July 2019
33. **Week day FDP** in Internet of Things, NPTEL, July-Oct 2019
34. **Two weeks FDP** on Entrepreneurship Development, organized by BVRITH in collaboration with Center for Entrepreneurship Development (CED, ALEAP-INDIA) and NSTEDB, Govt. of India, 18<sup>th</sup> to 30<sup>th</sup> March 2019.
35. **One week FDP** in Introduction to Research by NPTEL, Feb-April, 2019
36. **Week day FDP** in CMOS Digital VLSI Design NPTEL, Feb-April, 2019
37. **One week workshop** on “Partial Reconfiguration flow using XILINX Tools" organized by Department of ECE, BVRITN in association with Xilinx University Program, CoreEl Technologies, 19<sup>th</sup> to 23<sup>rd</sup> Sep 2017
38. **Faculty Development Program** on “Real Time Applications of Communication Theory” in association with “Qcares Team” Qualcomm at VEDIC, Hyderabad, 8th & 9th July 2017.

### ***Workshops Attended***

1. **National level Workshop** on “Origin Software”, organized by Department of BS&H, BVRIT HYDERABAD College of Engineering for Women, on 28<sup>th</sup> May 2022.
2. **One day workshop** on “Deep Learning Series” organized by AI Pathshala, K-Tech, NASSCOM and MathWorks. 2 December 2021.
3. **Three day Workshop** "Embedded Systems - An Application Driven Approach" organized by the All India Council for Technical Education (AICTE), ATAL Academy, STMicroelectronics and Arm Education, 25<sup>th</sup> -27<sup>th</sup> Aug 2021
4. **One day workshop** on “Intellectual Property Rights (IPR)” organized by University of Hyderabad- Institutional Innovation Council and Technology Enabling Centre, 22nd May 2021

5. **Three Days National Level Workshop** on “High Speed CMOS Design” organized by Pantec-E learning, 21st – 23rd Jan 2021
6. **Three days national level IEEE workshop** on Augment and Virtual Reality using unity-3D, organized by Panimalar Institute of Technology Chennai in Association with IEEE SB-PIT & Pantech, 28<sup>th</sup> – 30<sup>th</sup> Dec 2020
7. **One day professional development program** on “Algodynamics-An Approach to Teaching Algorithms”, Organized by IEEE Education Society Chapter, IEEE Hyderabad Section in collaboration with SERC, IIIT Hyderabad, 3<sup>rd</sup> October 2020.
8. **Three day workshop** on “IC design”, organized by Entuple Technologies, 2nd to 4th Sept. 2020
9. **Week day workshop** on "Electronics Euphoria", By **AIC ALEAP**, WE-Hub, 19th to 25th August 2020
10. **One week workshop** on "Applied IOT with Node MCU-Raspberry pi & cloud Configuration", organized by **HITAM**, 21st to 25th July 2020
11. **Two-day workshop** on “Research and Ethics”, **R&D, BVRITH** in collaboration with **IEEE Hyderabad Section & GRSS**, 28th & 29th May 2020
12. **Two day workshop** on Intellectual Property Rights, by MSME-Technology Development, 18th & 19th May 2020
13. **One Day workshop** on “ Artificial Intelligence / Machine Learning / Deep Learning”, by **NVIDIA, Digital Track Solutions Private Limited**, 15th May 2020
14. **Two Days Workshop** on ML and AI Using Covid 19 Virus Data Analysis, In Association with **NSS IIT Roorkee** 15th May 2020
15. **Three Days Workshop** on IOT and Cyber - organized by **VEDIC& IEEE CS chapter, VEDIC Bangalore**, 13<sup>th</sup> to 15<sup>th</sup> Dec 2019
16. **One day workshop**, by IEEE Hyderabad **SECTION STUDENT CONGRESS'19**, at Jawaharlal Nehru Technological University Hyderabad, Hyderabad, India, 10<sup>th</sup> August, 2019
17. **Two day workshop** on Curriculum Design, by Dr. Shiva Kumar, **VEDIC**, Hyderabad, 18th -19th July 2019
18. **One day workshop** on “Unconscious Bias at Work Place” organized by Dr. Bala Krishna, **VEDIC**, Hyderabad, 8th Feb 2019
19. **Two day Workshop** on Faculty Colloquium by Dr. Shiva Kumar, **VEDIC**, Hyderabad, 13th & 14th Dec 2018
20. **Two day Workshop** on Course Design by Dr. Shiva Kumar at **VEDIC**, 26th & 27th June 2018
21. **One day workshop** on TTT, A Learned various Web resources for active teaching and innovative learning procedures organized by Dr.D. Lakshmi at **VEDIC**, 22nd June 2018
22. **Three day workshop** on Scientific Educational Practices at **VEDIC**, 27th to 29th April 2017
23. **Three day workshop** on “Inspire – Impact – Introspect- Level-1” by **VEDIC**, Hyderabad, 29th-31st Aug, 2017
24. **Two day workshop** on “Teaching Learning Methodologies” by Dr.Vijaykrishnan Narayanan, **VEDIC**, Hyderabad, 26<sup>th</sup> to 27<sup>th</sup> Dec, 2017.

### ***Professional Memberships:***

- Lifetime member of Indian Society for Technical Education (ISTE), LM64189
- Lifetime member of Institute of Engineers and Technical Education (IETE), M-218887

- Senior Member of Institute of Electrical and Electronics Engineers (IEEE), SM 90763176

### ***Certifications***

- How to Write and Publish a Scientific Paper (Project-Centered Course), an online non-credit course authorized by École Polytechnique and offered through Coursera, 27<sup>th</sup> April 2020.
- R Programming an online non-credit course authorized by Johns Hopkins University and offered through Coursera, 30<sup>th</sup> April 2020
- What is Data Science? An online non-credit course authorized by IBM and offered through Coursera, 20<sup>th</sup> July 2020.
- Introduction to Psychology, an online non-credit course authorized by Yale University and offered through Coursera, 18<sup>th</sup> July 2020.
- AI for Everyone, an online non-credit course authorized by deeplearning.ai and offered through Coursera, 15<sup>th</sup> July 2020.
- How Google does Machine Learning, an online non-credit course authorized by Google Cloud and offered through Coursera, 18<sup>th</sup> August 2020
- IGEN'2020, IEEE SAC Committee, IEEE Hyderabad Section, 18-20 th Sep 2020.
- IPR Online Training, organized by MSME-TECHNOLOGY DEVELOPMENT CENTRE (PPDC), from 18.05.2020 to 19.05.2020

### ***Any Other Achievements***

- Got Certificate of Appreciation for Coordinating ATAL Online Faculty Development Programme, titled with "Machine Learning Applications in Micro-Nano VLSI Technologies" from 21/06/2021 to 25/06/2021.
- Certificate of appreciation for reviewing the paper for Eighth International Conference In Transformations in Engineering Education (ICTIEE-2021)
- Got recognition certificate as Reviewer IEEE Transactions on Circuits and Systems II: Express Briefs on 14-10-2021
- Got best paper award in International Conference on Advances in Signal processing Communications and Computational Intelligence with Publication partner as American Institute of Physics (AIP) organized by Dept.of ECE, CMR Technical Campus, Hyderabad, on 23rd-24thJuly2021
- IEEE Senior Member escalation award received on 15th Feb 2020
- Certificate of Appreciation for article published in e-flip magazine on 31<sup>st</sup> august 2020
- Invited for Presentation of paper titled with "Two Cell Fault Models and Parasitic RC Test Method for Embedded SRAM" has been selected for presentation at 2<sup>nd</sup> International Conference on Information and Communication Technology 2018 (ICICTM'18), from Malaysian Institute of Information Technology during May 3rd -4th, 2018
- Best Paper Award in IEEE International Conference On Computational and Characterization Techniques in Engineering & Sciences (CCTES-18) September 14-15, 2018, Organized by Department of ECE, EE & Physics, Integral University, Lucknow.
- Reviewer and Advisory / Technical Program Committee member for INSC (Institute of Scholars) Journals as well as conferences since Dec 6, 2019.



- Online Certifications from Coursera, NPTEL platforms in the subjects of Digital Circuits, CMOS Digital VLSI DESIGN, and Introduction to Research, Internet of Things, Microelectronics : Devices To Circuits, Introduction to Psychology, AI for Everyone, R Programming, IBM-What is Data Science, How to write and publish a scientific Paper.
- Invited as Judge for PARICUS DE TECHNICAЕ, (paper presentation) Techno Cultural Fest SPOORTHИ, JNTUCEH, 2013.

➤ **Research Contributions**

- Guiding Ph.D Scholar M.Venkatesham, in the field of VLSI Design for the domain area of Embedded memory testing using ML Algorithms, from Lovely Professional University (2019 batch)
- **Reviewer:** IEEE Transactions on Circuits and Systems II, IETE Journal of Research, International Journal of High Performance Systems Architecture, IJECE, IJETI (International Journal of Engineering and Technology Innovation), and IJVLSICS.
- Citations: 41, h-index-4, i-index-1 (Scopus-7, US Patent citations-2)
- Attended 7<sup>th</sup> International Conference on Transformations in Engineering Education (ICTIEE-2020), on 5<sup>th</sup> to 8<sup>th</sup> Jan 2020, organized by Anurag Group of Institutions, Hyderabad.