## Personal Information

| Name | Ms. M. Praveena |
|---|---|
| Years of Experience | Teaching:<br>16 Years 6 Months |
| Email Id | praveena.m@bvrithyderabad.edu.in |
| Areas of Specialization | VLSI, Optimization Algorithms |

## Educational Qualifications

| Doctoral Degree | Ph.D. | Pursuing |
|---|---|---|
| PG Degree | M.Tech. | VLSI System Design |
| UG Degree | B.Tech. | Electronics & Communication Engineering |

## Papers Published

### International Journal Publications

1. Praveena M, Sujatha B, Theertha Sree Y, Neelima D, Manasa Y, "Heart Rate Monitoring from ECG signal using Discrete Wavelet Transform", Gradiva Review Journal, Volume 7, Issue 6, 2021, pp: 127-132.
2. Radha Abburi, Manne Praveena, R Priyakanth, "Tinkercad – A Web based Application for Virtual Labs to help Learners Think, Create and Make", Journal of Engineering Education Transformations, Volume 34, January 2021, Special Issue, eISSN 2394-1707, pp: 535-541.
3. R Priyakanth, Radha Abburi, Manne Praveena, "Design and Impact of Interactive Video Content for the Improvement of Student Engagement and Learning", Journal of Engineering Education Transformations, Volume 34, January 2021, Special Issue, eISSN 2394-1707, pp: 518-523.
4. M. Praveena, N. Balaji, C. D. Naidu, "Hardware efficient Block Matching algorithm based on modified differential evolution optimization for fast motion estimation", Analog Integrated Circuits and Signal Processing, Springer, August 2019, Volume 100, Issue 2, pp: 389-404.
5. Manne Praveena, Dr. Narayanam Balaji, Dr. Challa Dhanunjaya Naidu, "FPGA Implementation of High Speed Medical Image Segmentation using Genetic Algorithm", Journal of Theoretical and Applied Information Technology (JATIT), ISSN 1992 - 8645, 15th July 2017, Vol. 95, Issue No. 13,pp: 2981-2988.
6. M. Praveena, Dr. N. Balaji, Y. Sai Radhika, S. Vyshnavi, "VLSI Implementation of High Speed Optimized Image Segmentation using Parallel Prefix Adder", International Journal of Emerging Trends in Engineering & Development (IJETED), ISSN 2249 – 6149, Issue 7, Vol. 3, May 2017, pp 83-92,.
7. Neeraja.Ch, Praveena.M, Priyanka.V, "FPGA Implementation of Lossless Compression of Medical Images Using VlSI – Oriented Felics Algorithm", International Journal of Research in Information Technology (IJRIT), ISSN 2001-5569 Volume 1, Issue 12,

December, 2013, Pg. 264-273

8. M Srinivas, M Praveena, "Design of a Modified Gabor Filter by Using Verilog HDL", International Journal of Engineering Research and Applications (IJERA), ISSN: 2248-9622, Vol. 2, Issue 3, May-Jun 2012, pp.3183-3187.

## *International Conference Publications*

1. Manne Praveena, Rama Lakshmi Gali, Dr. V Hindumathi, "Improving Student Engagement & Learning using Formative Assessment Techniques", Mini Symposium on Effective Practices in Teaching-Learning (EPTL) by Indo Universal Collaboration foe Engineering Education (IUCEE), 23$^{rd}$ & 24$^{th}$ April 2022.
2. Manne Praveena, Rama Lakshmi Gali, V Hindumathi, "A Study on the Effectiveness of Formative Assessment Activities on Student Learning", 5th International Conference on Interdisciplinary Academic Research, Education and Innovation (ICIAREI-2022), 5th & 6th March 2022.
3. Manne Praveena, Dr. N Balaji, Dr. C D Naidu, "FPGA Design of Area Efficient, Superfast Motion Estimation using JAYA Optmization based Block Matching Algorithm", 7th Series of International Conference on Information System Design & Intelligent Applications – INDIA-2022, 25th & 26th February, 2022.

## *FDPs - Attended*

1. Participated in Two-Week Online UGC sponsored Refresher course on "Internet of Things" held from 9$^{th}$ to 24$^{th}$ August 2021 by UGC-HRDC, JNTUH Hyderabad and successfully completed with A+ grade.
2. Attended ATAL Online Elementary FDP on "Machine Learning Applications in Micro-Nano VLSI Technologies", from 21$^{st}$ to 25$^{th}$ June 2021 at BVRIT HYDERABAD College of Engineering for Women.
3. Completed a course with distinction on "Online Teaching" conducted by IUCEE Team of Faculty from July to September 2020, organized by IUCEE.
4. Attended a One Week FDP on "Wireless Communications & Mobile Technologies" organized by ECE Dept., BVRIT HYDERABAD College of Engineering for Women in association with QCares, Qualcomm from 6$^{th}$ to 10$^{th}$ July 2020 at BVRIT HYDERABAD College of Engineering for Women.
5. Attended a One Week FDP on "Hardware Modeling Using Verilog" organized by NPTEL- AICTE from Aug-Oct 2019.
6. Attended a 3 Day FDP on "Effective Engineering Teaching in Practice" organized by NPTEL – AICTE from Jan-Feb 2019.
7. Participated in One Week Faculty Development Program on "Open-Source Software through Spoken Tutorials, IIT Bombay" organized by Department of IT, BVRIT Hyderabad College of Engineering for Women, from 4$^{th}$ to 10$^{th}$ November, 2016.
8. Attended a Three-day FDP on "Emerging Opportunities for Research and Development

in VLSI ", organized by Department of ECE, 19$^{th}$ to 21$^{st}$ October 2016, under IEEE SB, BVRITH.

9. Attended an AICTE Sponsored Two-week Faculty Development Program on "Recent Trends & Advancements in VLSI for Embedded Applications" organized by Dept. of ECE, CBIT, Hyderabad from 15$^{th}$ to 27$^{th}$ June 2015.

## FDPs – Conducted

1. Organized and acted as Resource person for a Two Day online FDP on Virtual Labs "Fiddle with Virtual Components" on 4$^{th}$ & 5$^{th}$ September 2020 at BVRIT HYDERABAD College of Engineering for Women, Bachupally, Hyderabad.

2. Organized a Faculty Development Program on "Full Custom IC Design and FPGA Design Flow" on 23$^{rd}$ – 25th February 2012 at VNR Vignana Jyothi Institute of Engineering & Technology, Hyderabad.

3. Organized a 2 week Staff Development Program on "Real Time Signal and Image Processing "from 6$^{th}$ to 18th June 2011 at VNR Vignana Jyothi Institute of Engg & Tech, Hyderabad.

## Workshops – Attended

1. Participated in National level Online workshop on "Origin Software" on 28$^{th}$ May 2022, organized by Department of Basic Sciences & Humanities, BVRIT HYDERABAD College of Engineering for Women.

2. Participated in a one-day workshop on "Unconscious Bias in the Workplace" on 1$^{st}$ May 2019 conducted by VEDIC, Hyderabad.

3. Participated in a three-day workshop on "Inspire-Impact-Introspect-Level 1" held during 18$^{th}$ – 20$^{th}$ March 2019 at VEDIC, Hyderabad.

4. Participated in a three-day workshop on "Think-Technology-Transform-Level 1" held during 19$^{th}$ – 21$^{st}$ April 2018 at VEDIC, Hyderabad.

5. Attended a one-week workshop on "Integrated Circuit & System Design using CAD Tools" organized by ECE Dept., VNR Vignana Jyothi Institute of Engineering & Technology from 12$^{th}$ to 17$^{th}$ June 2017.

6. Participated in a two-day workshop on "Mind Map for Effective Learning / Teaching" held during 6$^{th}$ – 7$^{th}$ June 2017 at VEDIC, Hyderabad.

7. Participated in TEQIP-II sponsored workshop on "Embedded Hardware, Operating Systems & IoT Applications" organized by Department of ECE, VRNVJIET during 20$^{th}$ to 23$^{rd}$ January 2016.

8. Participated in a two-day workshop on "Sensors, Data Acquisition and Automation Control" under IEEE student branch, conducted by VI solutions, Bangalore, organized by Dept. of ECE, BVRITH in association with National Instruments Systems Pvt. Ltd., Bangalore on 12$^{th}$ & 13$^{th}$ September 2014.

9. Participated in "Assistive Technology Solutions in Minutes" workshop by Dr.Therese

Willkomm, Director of AT in NH, University of New Hampshire, USA on 26<sup>th</sup> and 27<sup>th</sup> December 2013.

10. Participated in a "Two Day Workshop on EDA Tools in Chip Design" with hands on experience on Cadence SoC Encounter at TIIT Pvt. Ltd. in association with Infotech Enterprises Limited, Hyderabad on $17^{th} - 18^{th}$ February 2010.

11. Participated in Two day DSP workshop at PVP Siddhartha Institute of Technology, Vijayawada, held on $3^{rd}$ and $4^{th}$ March 2005.

## Events Organized

1. Organizing member, Master of ceremony & prepared Souvenir for INDIA 2022 conference conducted on $25^{th}$ & $26^{th}$ February 2022.

2. Organized a non-technical event "Green Flare" on $26^{th}$ September 2019 under department technical association, Electro Elitz.

3. Convener for "NYPUNYA 2K19", a National level Inter Engineering Collegiate Sports Fest conducted on $29^{th}$ & $30^{th}$ January 2019.

4. Convener of the event "Circuit Debugger" conducted as part of Engineers' Day celebrations on $15^{th}$ September 2018.

5. Organized an event "Making Satellite from Trash" as part of Space Week Celebrations in October 2017 under IEEE Student branch - UDBHAVA.

6. Co-Convener for International Conference on Micro Electronics, Electromagnetics & Telecommunications ICMEET 2k17, conducted at BVRIT Hyderabad College of Engineering for Women, on $9^{th}$ and $10^{th}$ September 2017.

7. Organized a Two-Day Colloquium on "Inspiring Young Engineers Aspiring for Startups", under IEEE Student Branch, Udbhava at BVRIT Hyderabad College of Engineering for Women, on $29^{th}$ & $30^{th}$ July 2016.

8. Organized an event "Best Engineer Award" as part of Engineers Day Celebrations, under IEEE Student Branch, on $14^{th}$ & $15^{th}$ September 2015 at BVRIT Hyderabad College of Engineering for Women.

9. Convener for a 2-day workshop on "Gaming Through Embedded Systems" conducted at BVRIT Hyderabad College of Engineering for Women in association with Jay Robotix Pvt Ltd. from $25^{th} - 27^{th}$ February 2015.

10. Convener for "Prashnothari-II", Quiz competition organized by Department of ECE, BVRIT Hyderabad College of Engineering for Women, Hyderabad on $15^{th}$ September 2014.

11. Convener for "Prashnothari", Quiz competition organized by Department of ECE, BVRIT Hyderabad College of Engineering for Women, Hyderabad on $13^{th}$ September 2013.

*Professional Memberships:*

| | |
|---|---|
| Life Member ISTE | LM62876 |
| Life Member | 234467 |
| Member IAENG | 177677 |

*Any Other Achievements*

1. Participated in VEDIC Teaching Learning Conclave (VTLC) on 7$^{th}$ May 2022 and presented a Formative Assessment Technique implemented in classroom.
2. Received Best Paper Award in ICTIEE 2021 for the paper titled "Design and Impact of Interactive Video Content for the Improvement of Student Engagement and Learning" from 8$^{th}$ to 10$^{th}$ January 2021.
3. Reviewer for ICTIEE 2021-Eighth International Conference in Transformations in Engineering Education.
4. Received Appreciation for the contributions towards acting as a Resource person- To Creating a Meaningful Alternative to Labs for Students in Vishnu Online Interactive Colloquium (VOICE) on 26$^{th}$ June 2020.
5. Reviewer for Springer Journal "Analog Integrated Circuits & Signal Processing", an SCI indexed Journal.
6. Received good teaching performance evaluations in all categories every semester.
7. Has been IEEE student branch counselor from 2016 to 2019.
8. Organized a National Level Colloquium under IEEE student branch at BVRIT Hyderabad in July 2016.
9. Co Convener for International Conference ICMEET conducted in September 2017 at BVRIT Hyderabad.
10. Completed the following certification courses –
    i. LaTeX for Everyone & Everything - Udemy
    ii. Matlab Bootcamp: Matlab coding, Algorithms & Simulink – Udemy
    iii. Change Agility in the Workplace: Become a Change Agent - Udemy
    iv. Digital Circuits (Top 5%) – NPTEL
    v. Cyber Security Training for Teachers - MOOCs
    vi. Introduction to Technology Enabled Learning - MOOCs
    vii. Outcome Based Education (OBE) & Academic Quality Assurance – Udemy
    viii. Productivity & Time Management for the Overwhelmed – Udemy
    ix. Introduction to Internet of Things & Embedded Systems – Coursera
    x. Programming for Everybody (Getting started with Python) – Coursera
    xi. Grammar & Punctuation – Coursera
    xii. Getting started with Essay Writing - Coursera
    xiii. Advanced Writing – Coursera