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Department of Electronics and Communication Engineering

BVRIT HYDERABAD College of Engineering for Women,
Rajiv Gandhi Nagar Road, Bachupally-500090

REPORT JULY-2016

About the Department

The Department of ECE was established in the year 2012 with an intake of 60, and could procure extension of intake to 120 from the academic year i.e., 2014-15. The Department is supported by well experienced Faculty who

are diversified in various domains like Communications, VLSI and Embedded Systems. Dr. J. Naga Vishnu Vardhan is heading the department since its inception. He has 12 years of teaching experience and his areas of interest are Signal Processing and Communications. Labs are established not only to serve the purpose of the prescribed curriculum but also to take up innovative projects by the students to meet the societal needs. The touchstone of the department is the promotion of more hands on facilities to students through conduction of mini projects, workshops, industrial visits and internships. Technical seminars are encouraged by the department to enhance the presentation skills of the students. Apart from technical activities like fests, students are encouraged to participate in Hackathons, paper presentations, mini project exhibitions and competitions conducted by various organizations like India Skills competition, IT Olympiad, L&T Techgium, My Gov.in, IoT Innovation challenge, Smart India Hackathon etc.

Vision

To emerge as a centre of academic and research excellence in Electronics and Communication Engineering and create globally competent women technocrats with a high degree of social consciousness in a holistic learning environment.

Mission

- > To provide quality education to the students with emphasis on training related to latest technologies as per industrial needs.
- > To impart research culture, professional ethics and moral values to the students by committed and competent faculty striving for excellence.
- To inculcate a perceptive alacrity in students to identify real life problems, formulate strategies and evolve into contextually effective solutions.

Programme Educational Objectives (PEOs)

After three to six years of graduation, the graduates of this programme will be able to

- ➤ PEO-1: Excel in contemporary problem solving in Electronics and Communication Engineering and the allied fields through the knowledge-base provided by the program.
- **PEO-2:** Demonstrate their technical, communication and leadership skills in professional environment or as entrepreneurs with social responsibility.
- **PEO-3:** Analyze the latest issues and technology growth in the field of Electronics and Communication Engineering and update their knowledge and skills accordingly through continuous learning.
- **PEO-4:** Demonstrate ethical and human values in multicultural and multidisciplinary environments.

Events conducted

by Dept. of ECE, BVRITH on 26th & 27th Feb 2016 to equip the faculty with latest technologies. The Digilent Analog Discovery TM design kit, developed in conjunction with Analog Devices Inc., is the first in a new line of all-in-one analog design kits that will enable engineering students to quickly and easily experiment with advanced technologies and build and test real-world, functional analog design circuits anytime, anywhere.



Annual day was celebrated on 17th March 2016.





ANNUAL DAY 2016





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National Sports Meet "Nypunya – 2K16" was organized at BVRIT Hyderabad on 15th & 16th March 2016





NYPUNYA-ZK16

NATIONAL LEVEL SPORTS FEST-BVRIT HYDERABAD





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Highlights/Seminar/Guest Lectures Conducted

- Chairman Sir interacted with III Year students on 20th February 2016.
- ➤ Few students of II ECE participated in the Students Fest at IIIT Hyderabad on 19th & 20th February 2016
- ➤ Five faculty from BVRIT Hyderabad Dr. J. Naga Vishnu Vardhan (ECE), Dr. A. Vasanthi (CSE), Mr. Gopi Krishna (Mech), Ms. S. Ramadevi (IT), and Mr. Ramana Reddy (EEE) attended faculty development program on Pedagogic and Personal Effectiveness from 7th to 9th March 2016 organized by SVES at VEDIC, Aziz Nagar.
- Faculty attended seminar on "Healthy Needs" at BVRIT Hyderabad on 12th March 2016.
- ➤ II and III B.Tech students attended test conducted by "Reach Cyber Technologies" on 12th March 2016 regarding internship
- A Paper Entitled "Optimal Placement of Wireless Sensor Networks for structural health monitoring" by Ms. S. Madhavi, Mr. Sanjay Dubey and Dr. J. Naga Vishnu Vardhan paper was accepted for IEEE International Conference on Smart Structures and Systems.
- > Training on Arduino for ATL registered II B. Tech students was successfully completed.
- Mini Project Exhibition of II ECE was held on 2nd April 2016.
- ➤ Farewell for IV ECE students was held on 4th April 2016
- ➤ Chairman interacted with IV B.Tech students and delivered farewell speech on 11th April 2016.
- An IOT Workshop "Deploying the Application into Cloud" was held at BVRIT Hyderabad on 11th and 12th April 2016

- ➤ A Session by SHE Team was conducted for all III B.Tech Students on 10th September 2015.
- ➤ 26 Students of II ECE A and B presented Papers, Projects at JNTU Sultanpur in the students' fest on 11th & 12th April 2016. Following is the achievements
 - Got First prize in Paper Presentation on Micro Electronic Pill, Vasundhara, Akshitha – PPT
 - Second Prize in Project Expo, Manisha, Priyanka and Bindu
 - First Prize in Circuitrix. Sandhya and Vijaya Lakshmi
- Dr. J. Naga Vishnu Vardhan Professor & HOD-ECE acted as an Examiner for B.Tech Major Project Viva-voce at Vardhaman College of Engineering, Hyderabad on 13th April 2016.
- Shankar Narayanan, Chief Operating Officer at Harita Techserv Ltd, Chennai visited campus on 27thApril 2016 along with Vice-Chairman and Dr. Rajiv Sukumaran.
- ➤ A Two day workshop on Raspberry Pi for IOT registered students in association with TASK and HYSEA was held at college on 5th & 6th February 2016.
- ➤ An Interaction Session on cognitive sciences by Dr. Brooke was held on 9th Feb 2016 on ATL ,Mr. Guruswamy(EEE) and Mr. Priyakanth (ECE) attended the talk at BVRIT Narsapur on 9th February 2016
- ➤ A Lecture on API Score by Dr. G K Viswanadh was held on 12th Feb 2016.
- ➤ IoT Maker Space Hackathon was conducted on 20th February 2016 at BVRIT Hyderabad

ACADEMIC TOPPERS

A.Y 2016-17

IV Years (2012-16 batch)



B. Sanjana: 86.67%



P. Sravya: 83.07%



M.K.Jyothi:81.33%



J Sandhya: 82.67%



T Ravali: 82.27%



G likhitha: 81.87%

II Years (2014-18 batch)



A.Rohitha: 82.9%



M.Harshitha: 82.9%



K.Krishna Priya: 82.3%

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